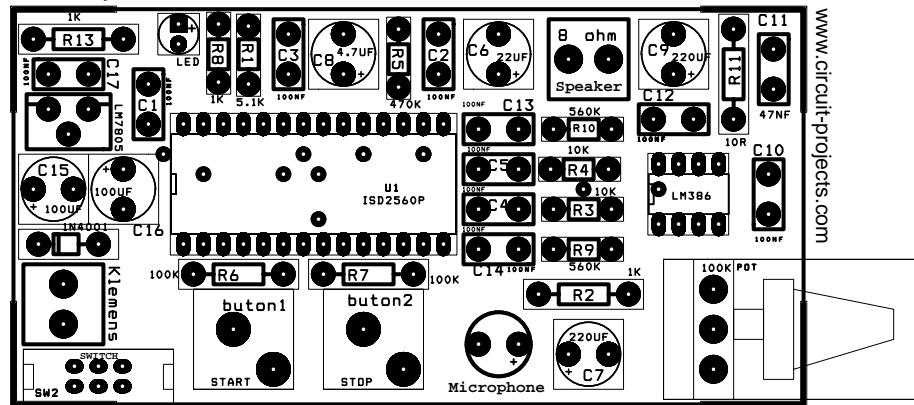


PCB Layout



Y.Erol